

*Amendments to the Specification*

Please make the following amendments to the specification.

[0023] The PLL circuit of FIG. 1 is shown for illustrative purposes only. The present invention may be applied to any PLL circuit as well as any other circuit including a charge pump. For example, the charge pump of the present invention may be applied to the PLL circuit of FIG. 17, the frequency synthesizer of FIG. 18, and a PLL 4506 of FIG. 46a, all of which are disclosed in the above mentioned '796 patent, U.S. Patent Application (Attorney Docket Number 41586/PBH/B600) entitled "Variable Transconductance Variable Gain Amplifier Utilizing a Degenerated Differential Pair," the contents of which have been incorporated by reference in full.